

ABSTRACT:

Method and apparatus for the synthesis of electronic circuits and, more particularly, to the synthesis of analog circuitry and mixed digital and analog circuitry, and related to the reuse of circuit designer knowledge for the simulation of mixed analog and digital circuitry to determine data points and to curve fit the data points to determine a polynomial equation that closely approximates simulated circuit performance, and related to the parameterization of circuit features with respect to circuit performance.

10

DRAFT - SUBJECT TO CHANGE